

# Stress-Minimizing Orthogonal Layout of Data Flow Diagrams with Ports

Ulf Rügge

*Kiel University*

Steve Kieffer

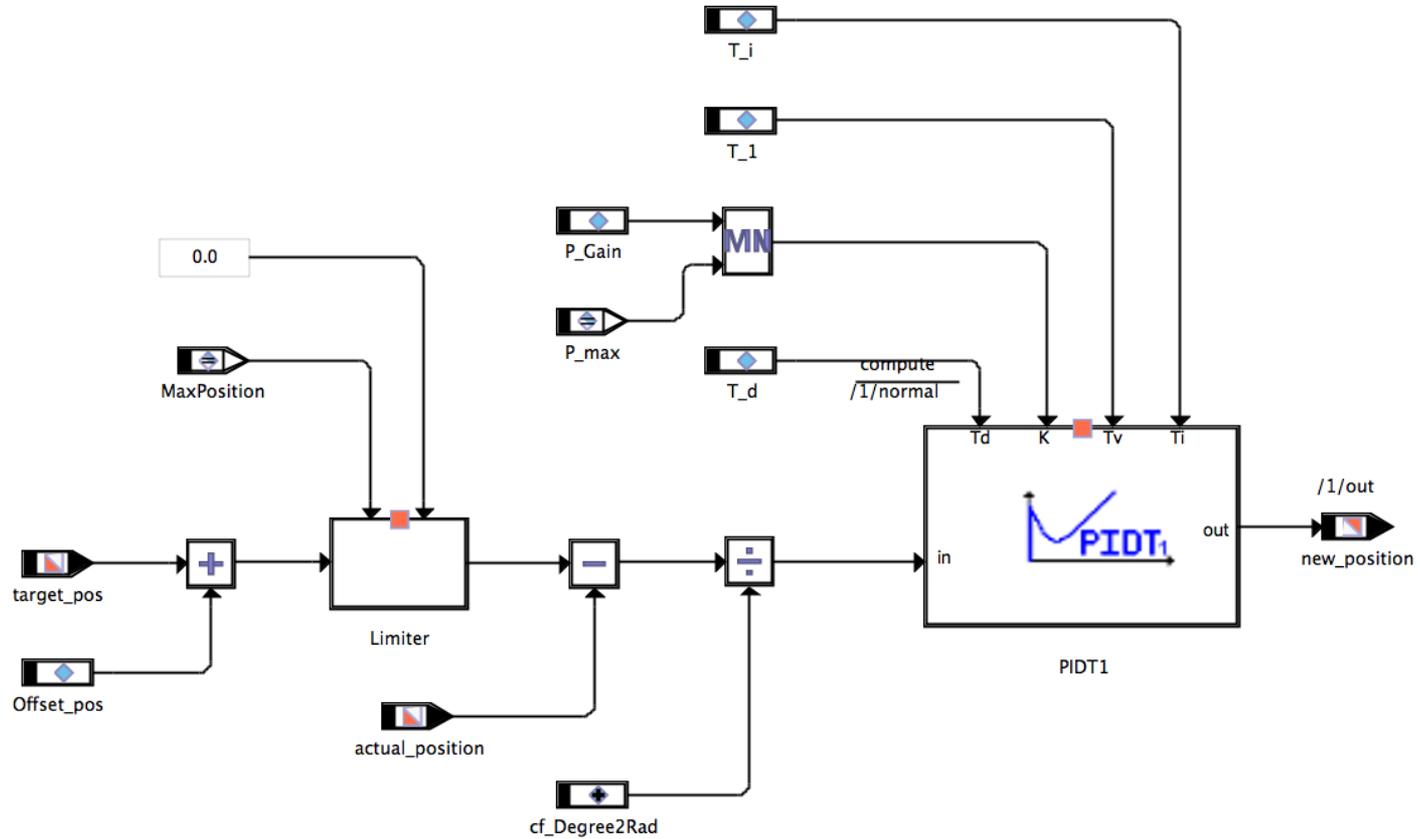
Tim Dwyer

Kim Marriott

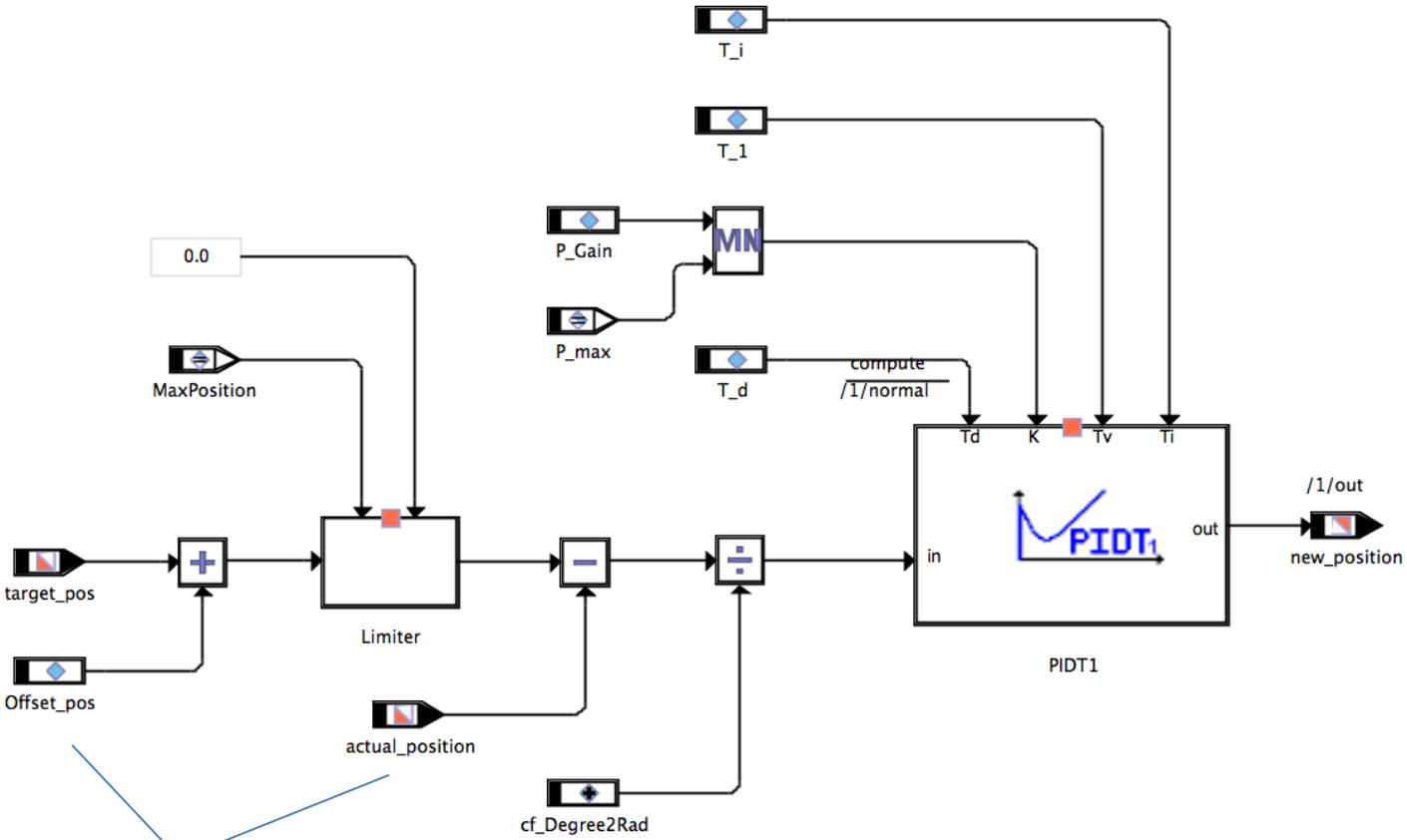
Michael Wybrow

*Monash University*

# Background: Automotive Industry

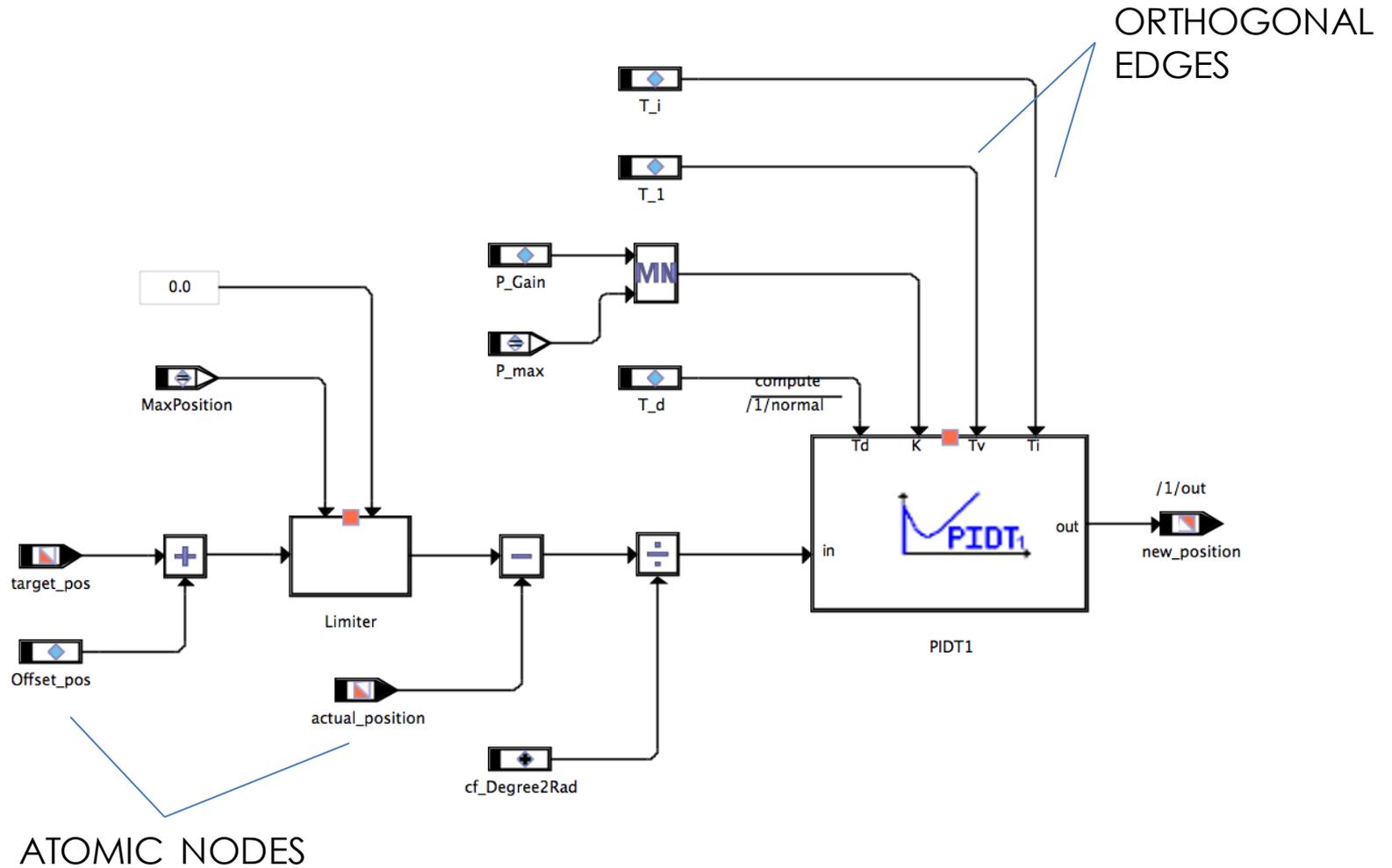


# Background: Automotive Industry

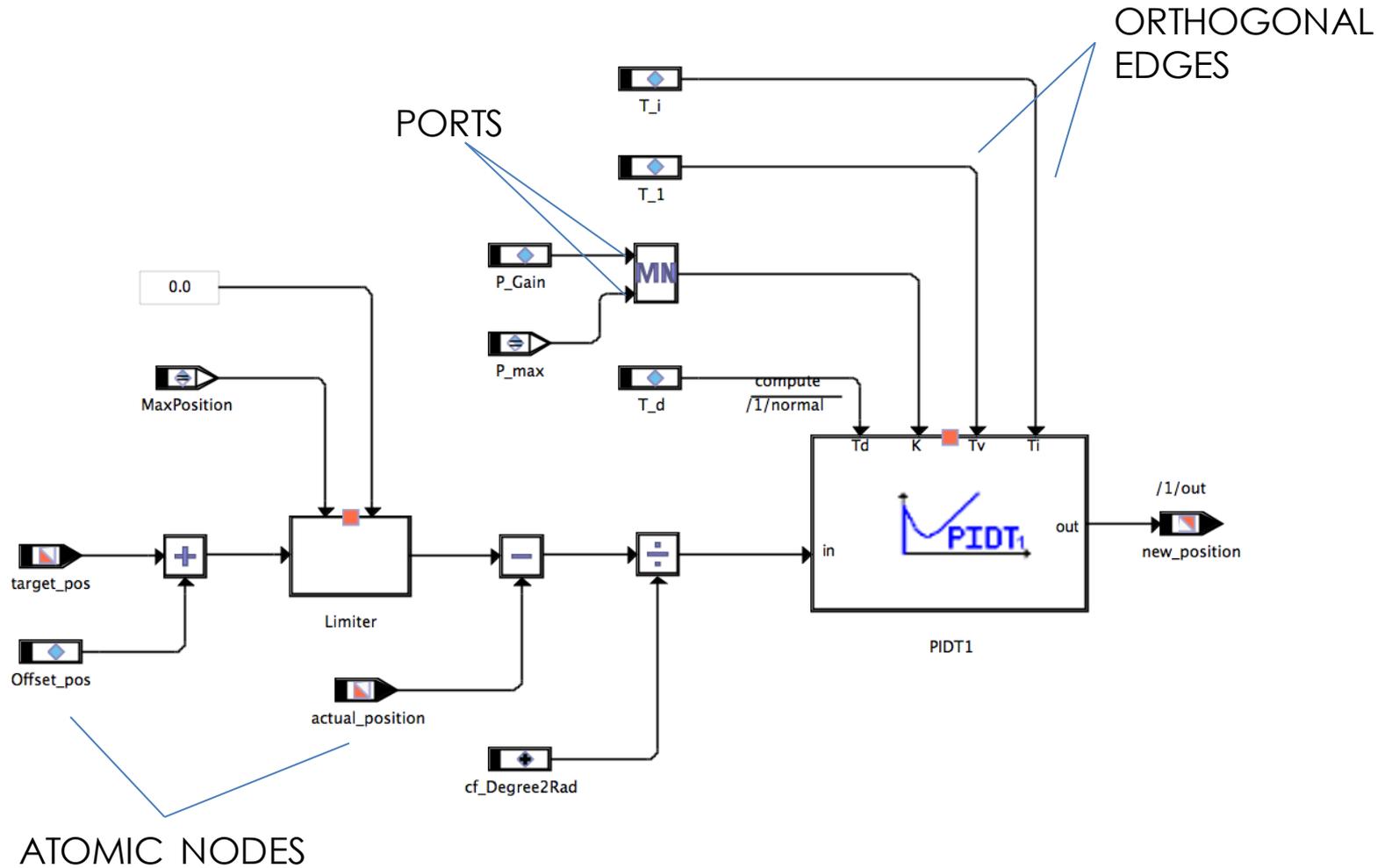


ATOMIC NODES

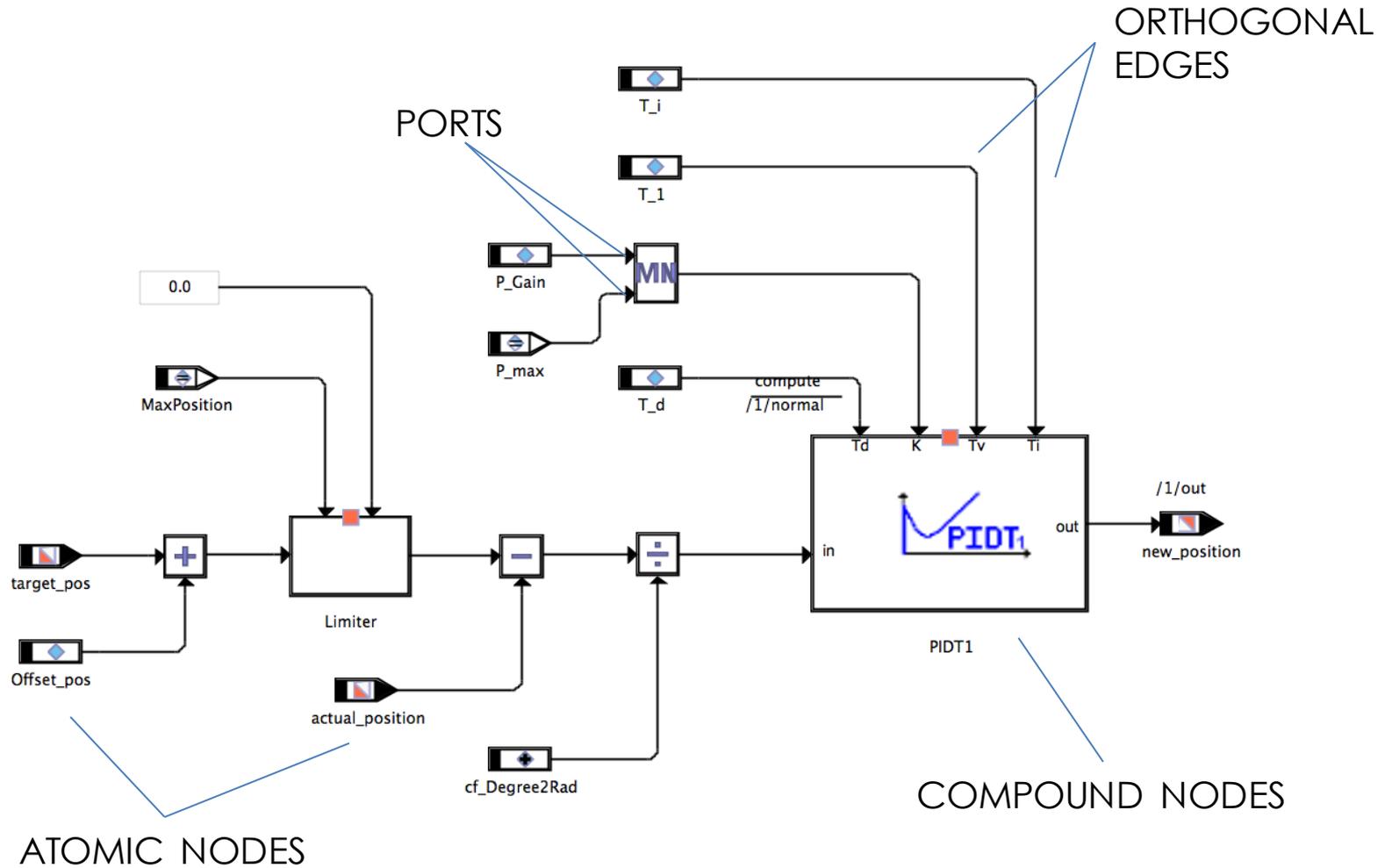
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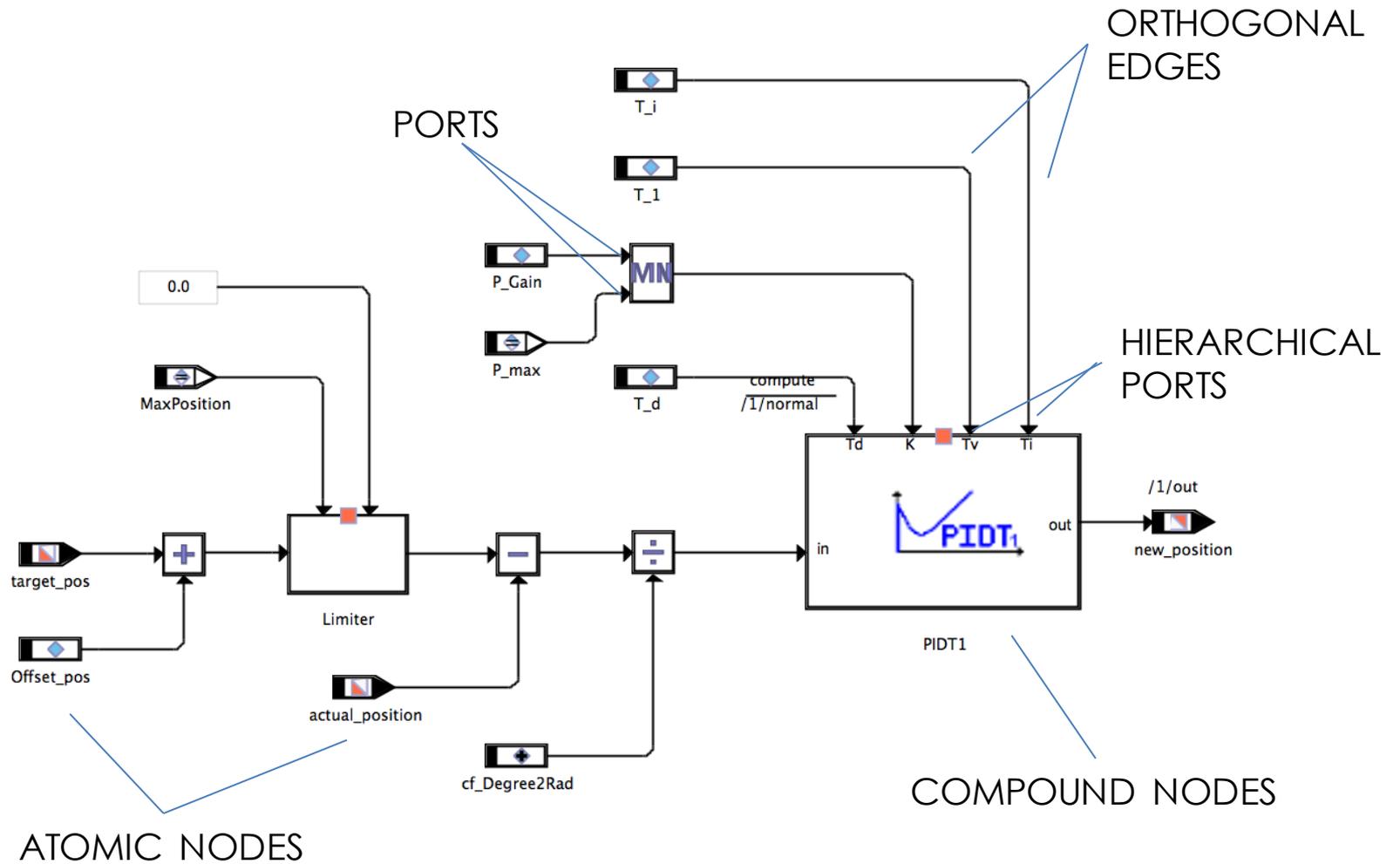
# Background: Automotive Industry



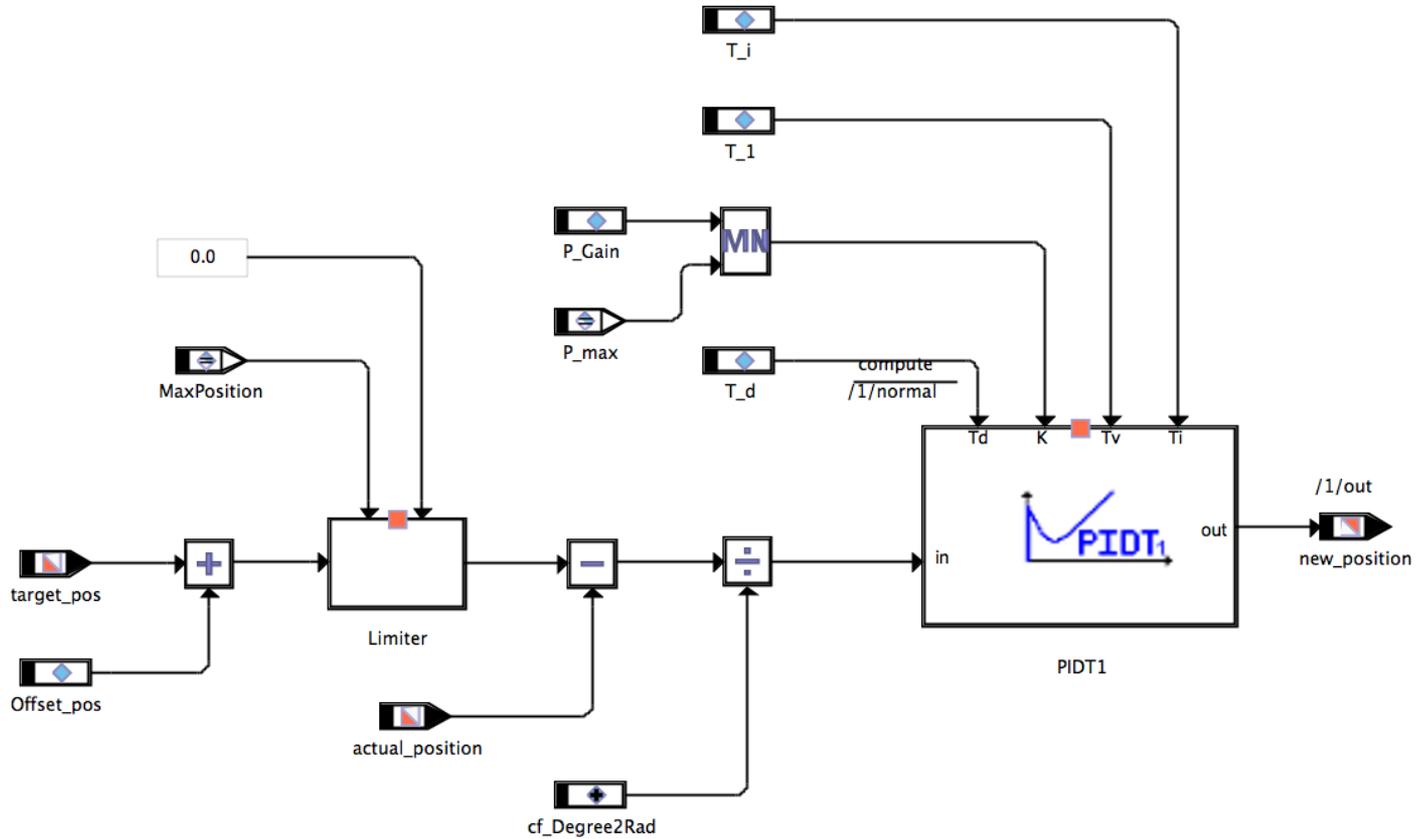
# Background: Automotive Industry



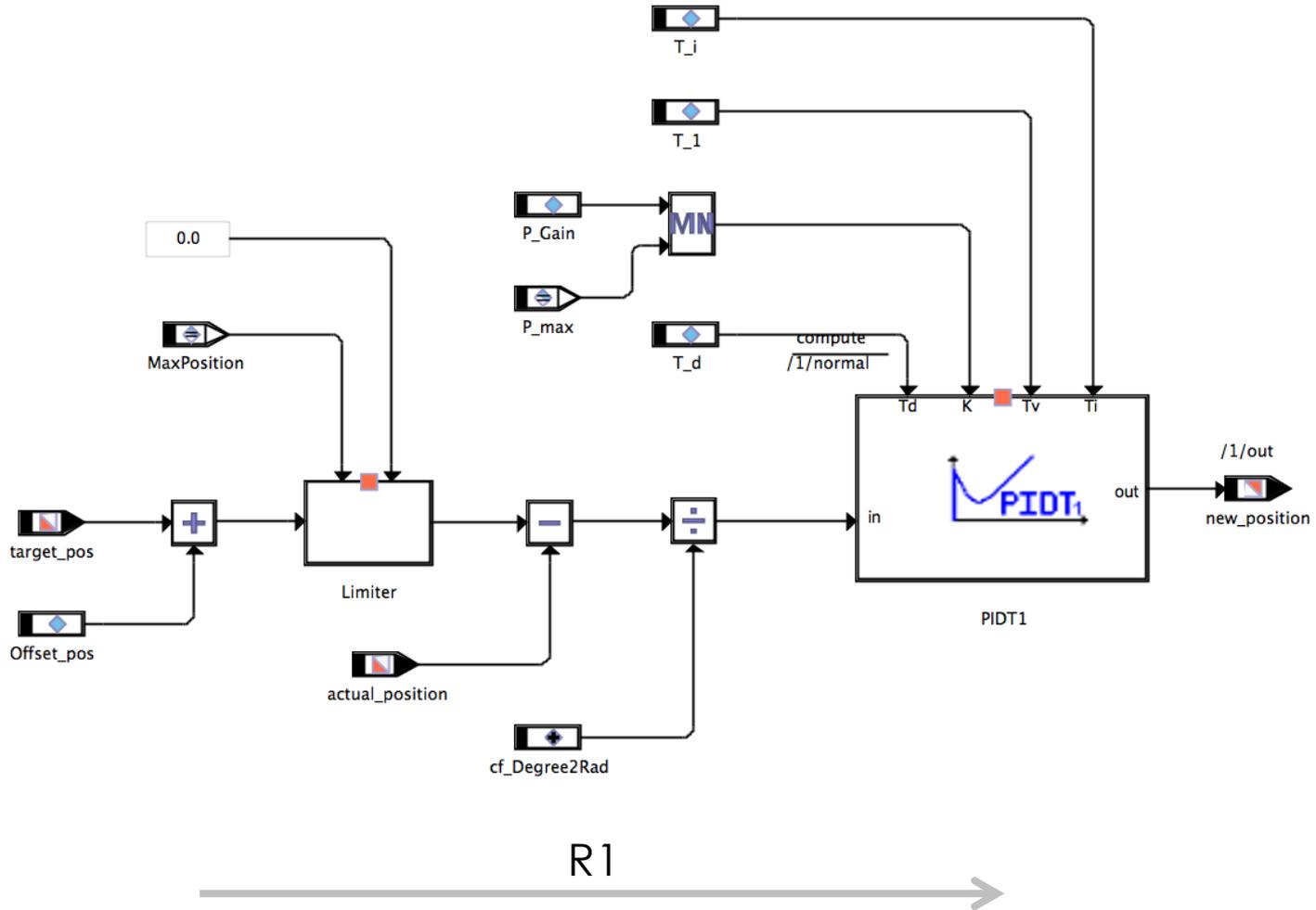
# Background: Automotive Industry



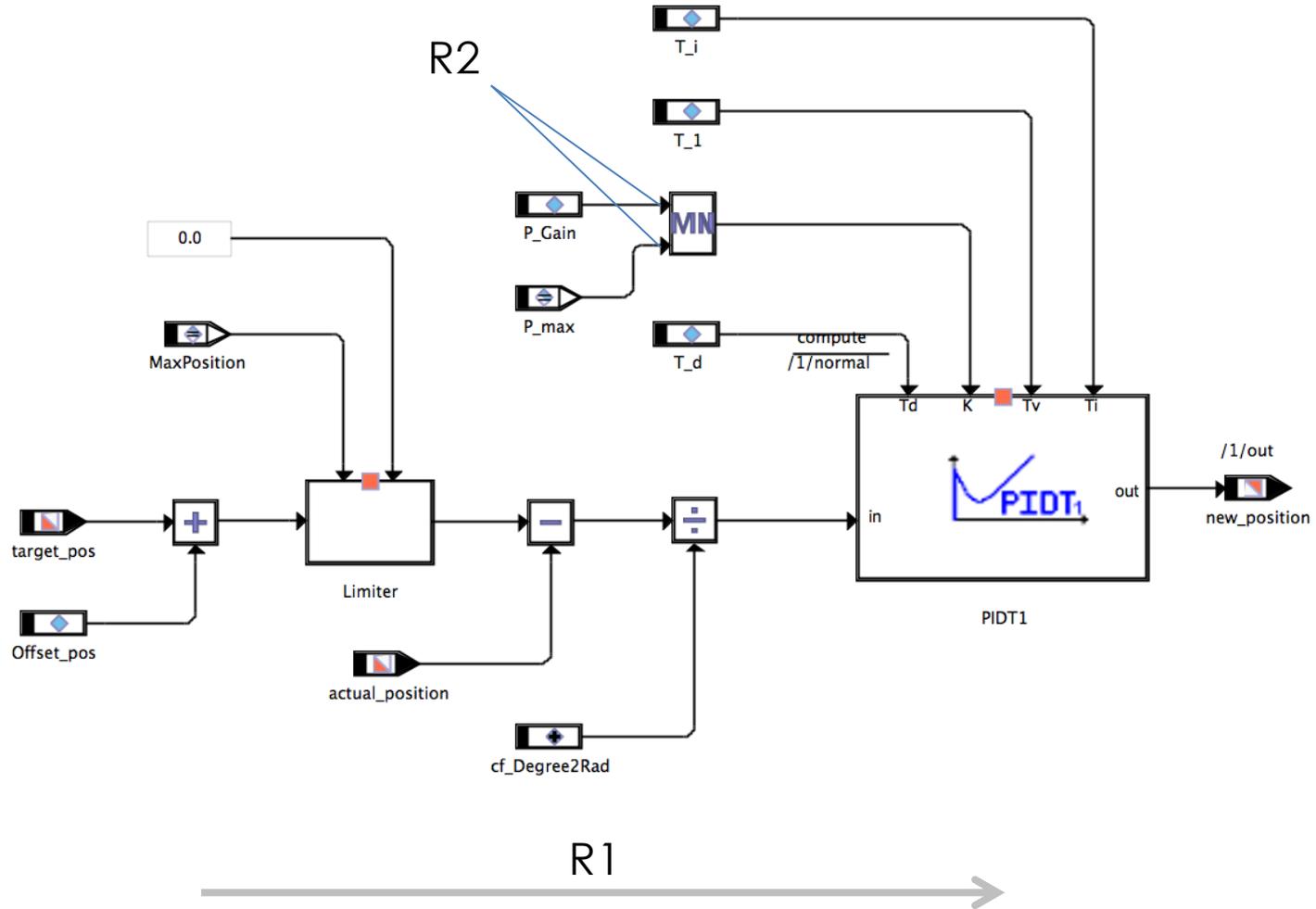
# Layout Requirements



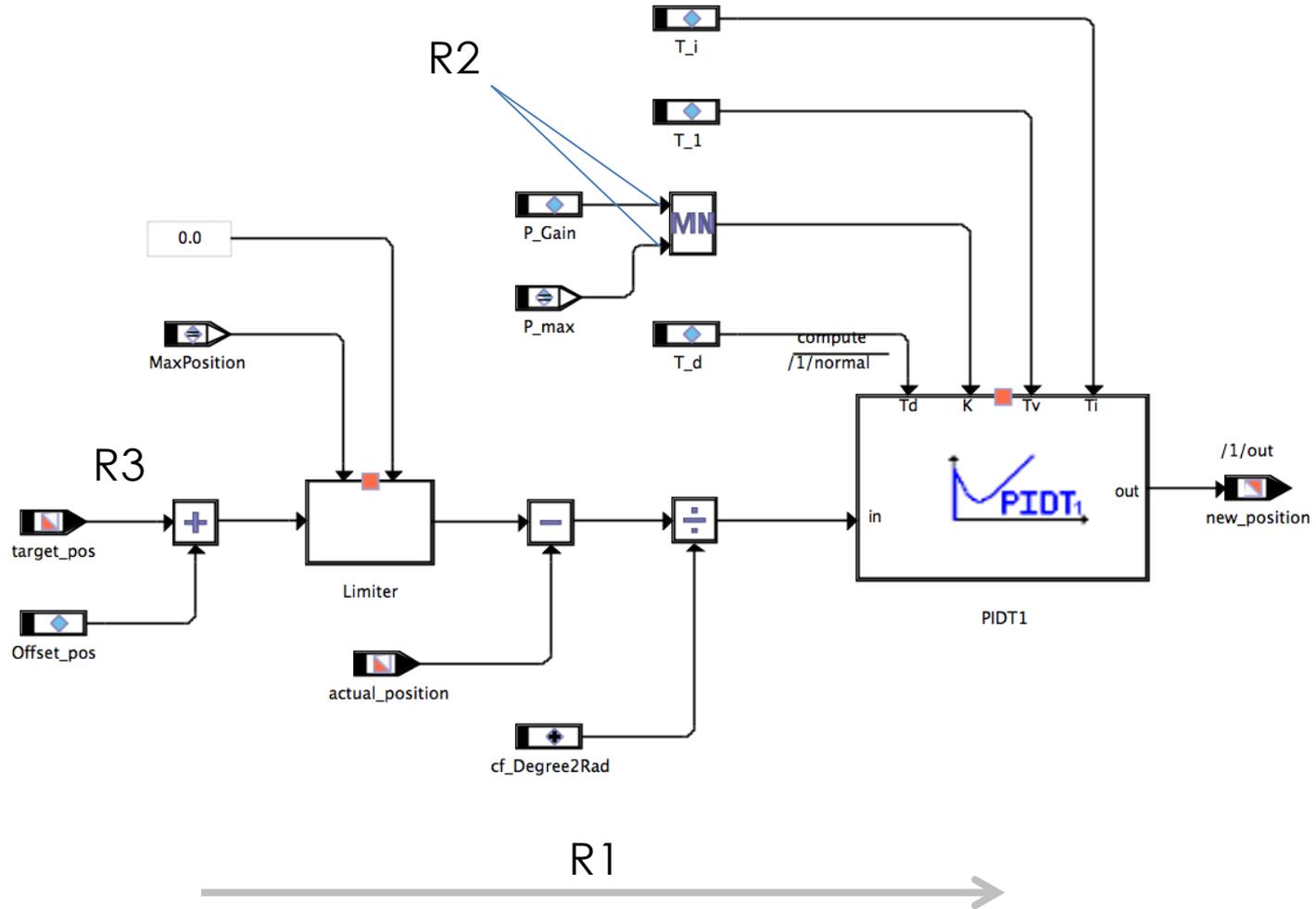
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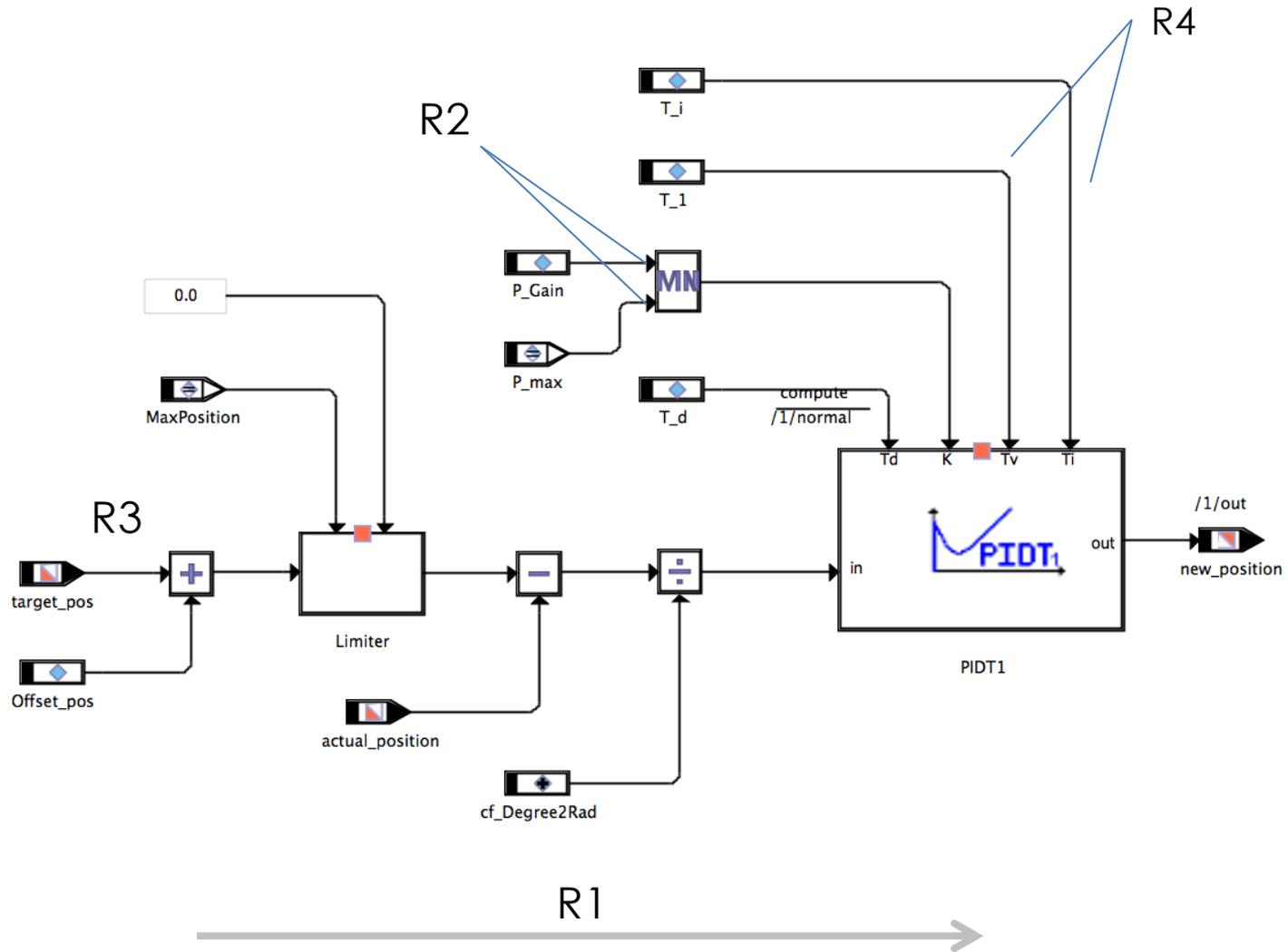
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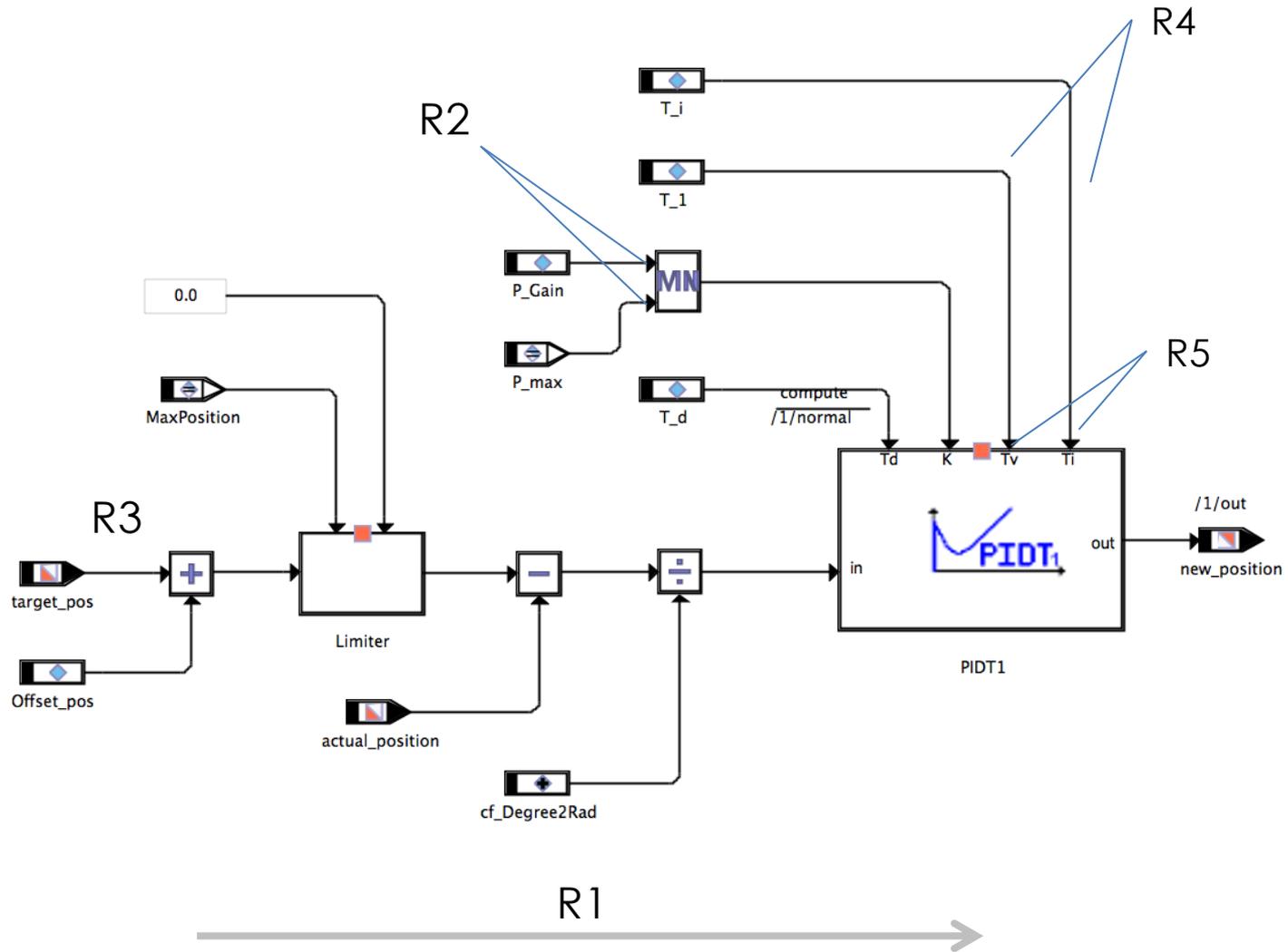
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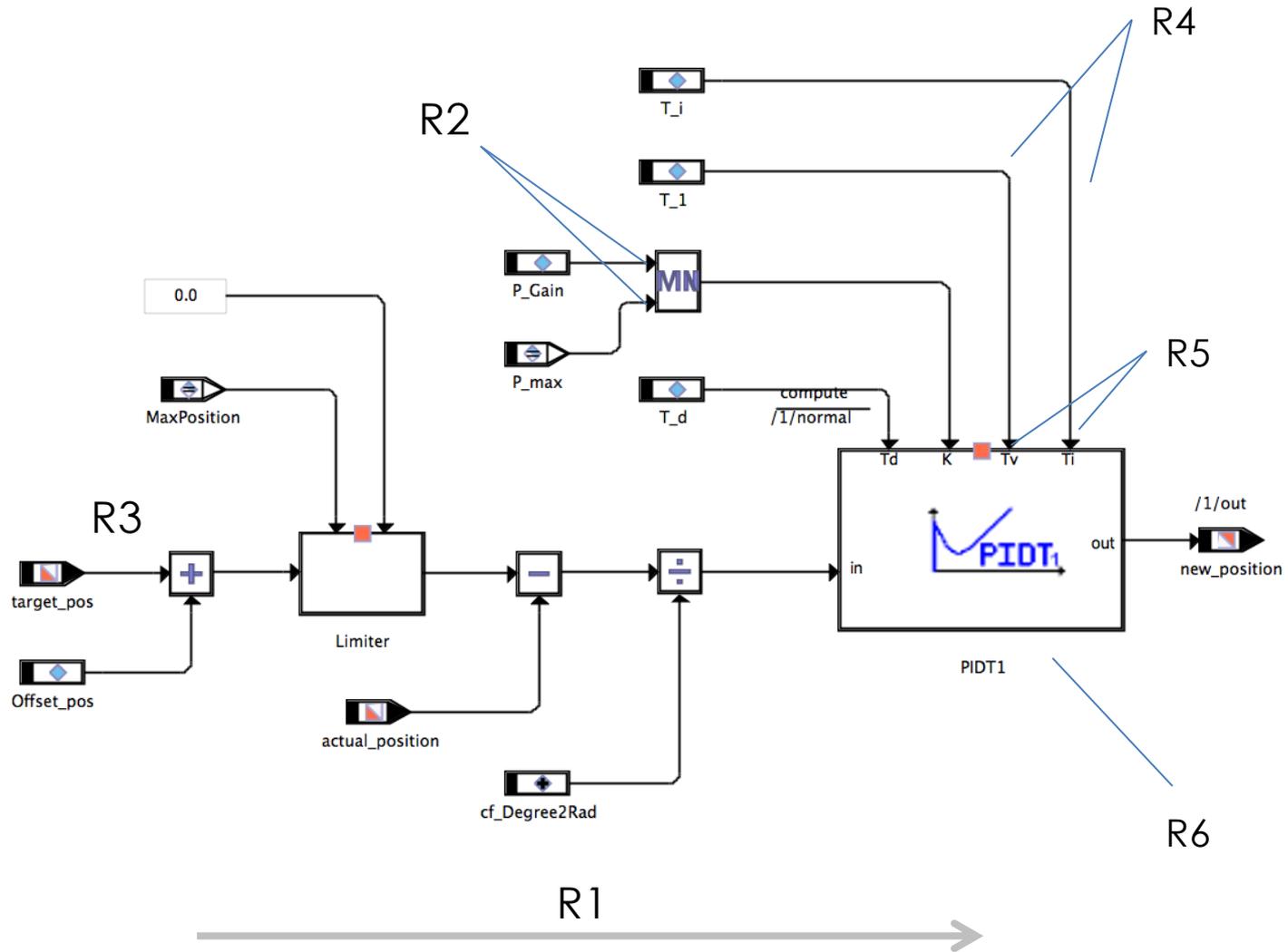
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# Current Layout Approach

- Use layer-based methods [Sugiyama et al. 81]
  - A lot of modifications (ports etc.) [Schulze et al. 14]

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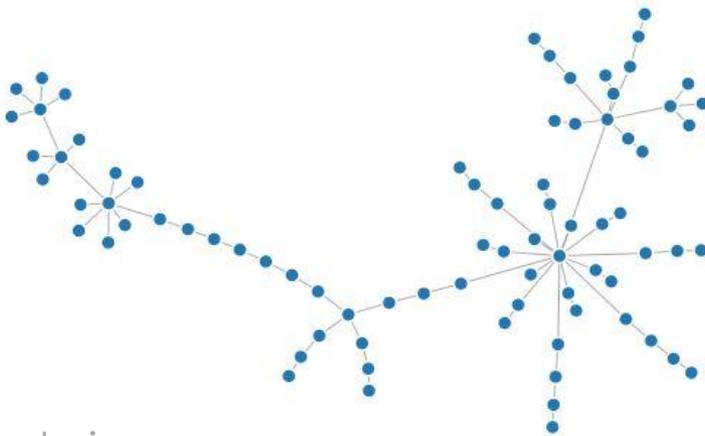
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    - In general satisfying layouts
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    - "Edge crossings not always important"
- Desired: simple/flexible solution

# New Approach

- Constrained stress minimizing layout

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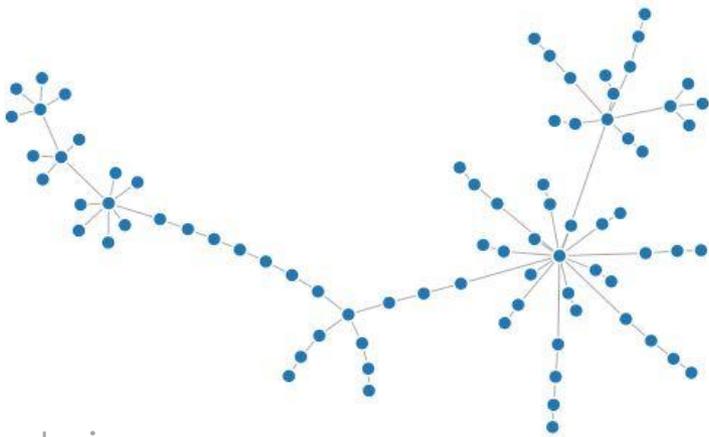
- Constrained stress minimizing layout
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cola.js

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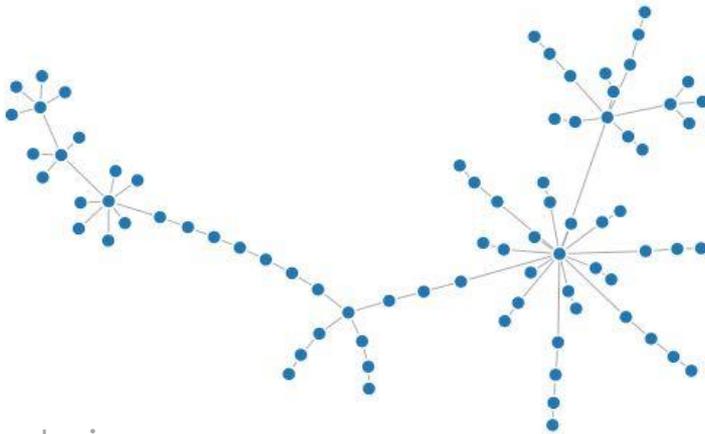
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  - Similar to force-directed approaches
  - Minimizes a single goal function



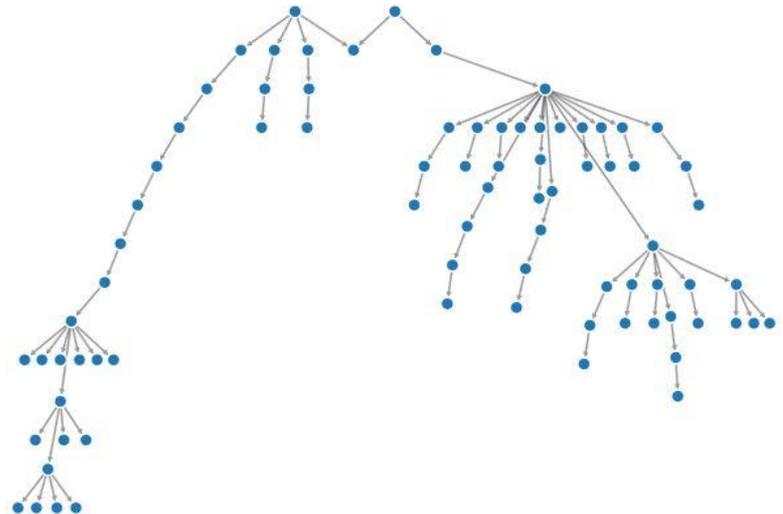
cola.js

# New Approach

- Constrained stress minimizing layout
  - Similar to force-directed approaches
  - Minimizes a single goal function
  - Subject to separation constraints



cola.js



# Goal Function: P-Stress

Minimize

$$\sum_{u < v \in V} w_{uv} ((lp_{uv} - b(u, v))^+)^2 + \sum_{(u, v) \in E} l^{-2} ((b(u, v) - l)^+)^2$$

*subject to certain constraints*

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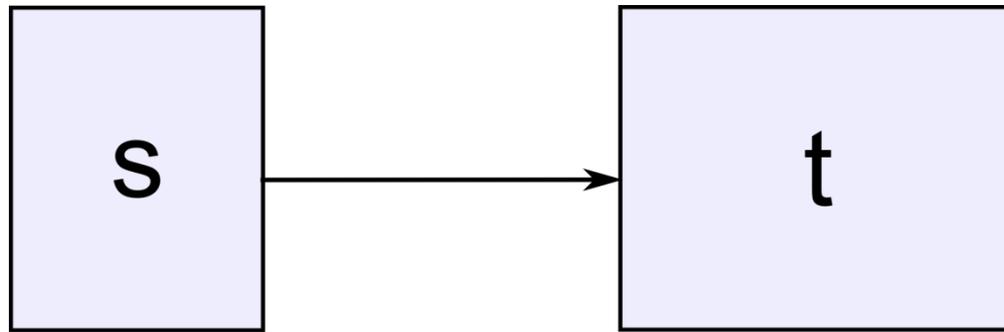
## INTUITION

- Nodes repulse each other up to a certain distance
- Edges contract until (individual) ideal length is reached

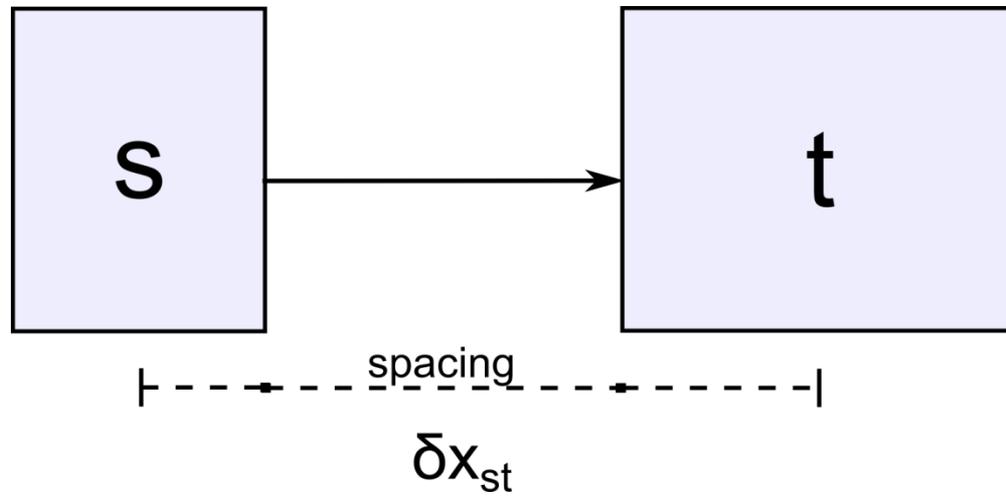
- $b(u, v)$  euclidean distance between  $u$  and  $v$
- $p_{uv}$  number of edges on shortest path between  $u$  and  $v$
- $l$  an ideal edge length
- $w_{uv}$  normalization factor
- $(z)^+$   $\max(0, z)$

[Dwyer et al. GD'09]

# R1 - Flow Constraints



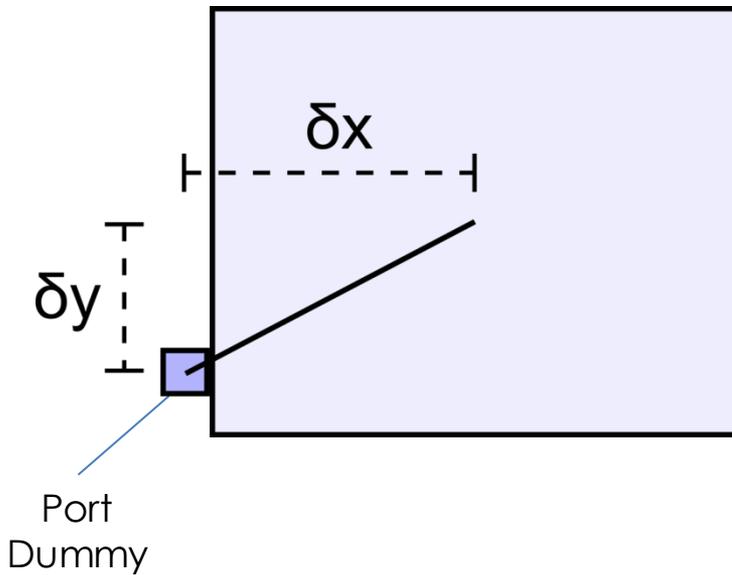
# R1 - Flow Constraints



$$x_s + \delta x_{st} \leq x_t$$

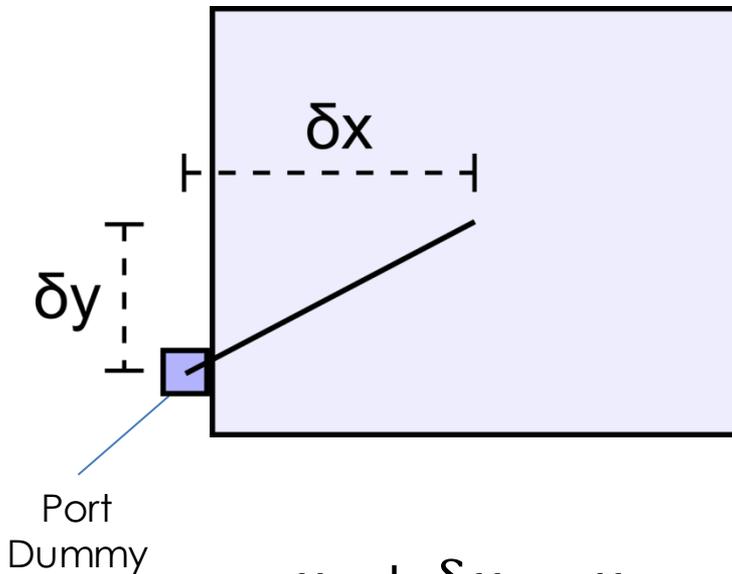
# R2 - Port Constraints

FIXED POSITION



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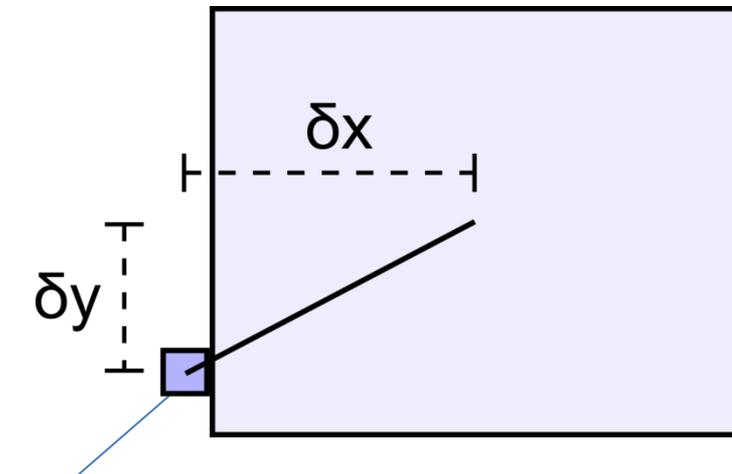


$$x_p + \delta x = x_n$$

$$y_p - \delta y = y_n$$

# R2 - Port Constraints

FIXED POSITION

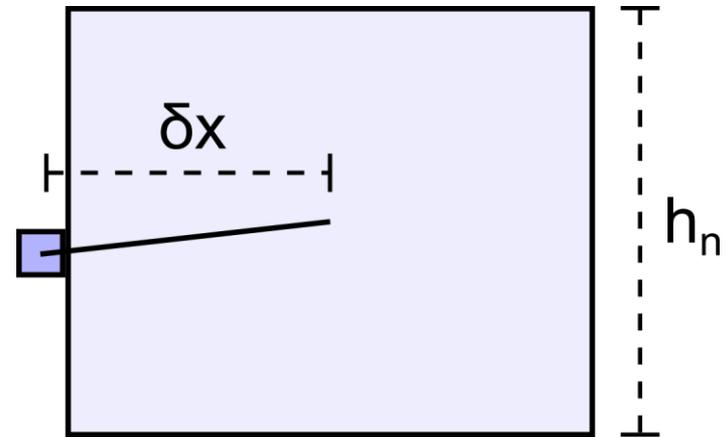


Port  
Dummy

$$x_p + \delta x = x_n$$

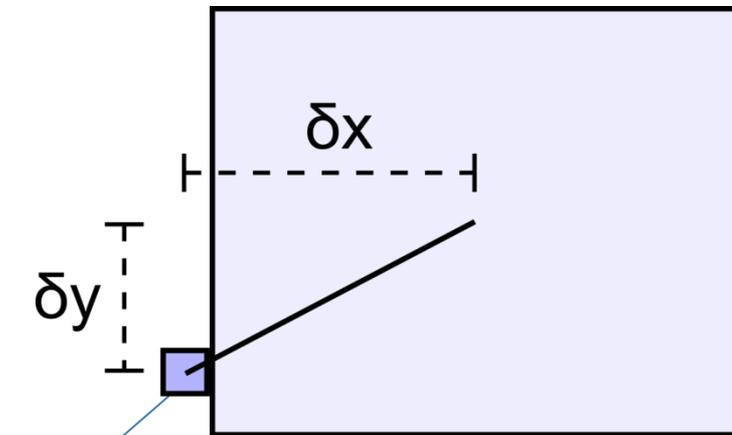
$$y_p - \delta y = y_n$$

FIXED SIDE



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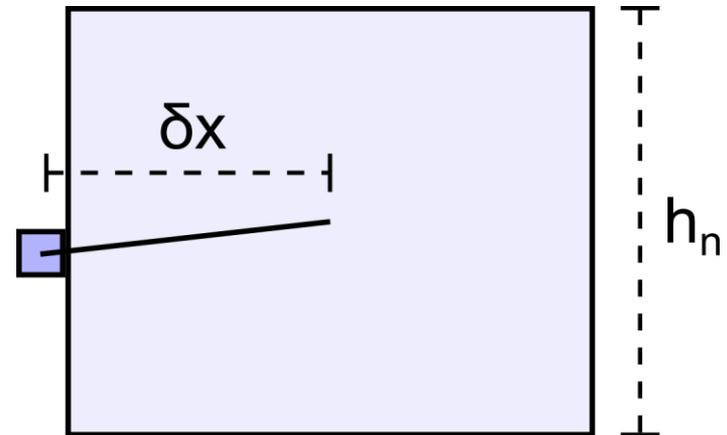


Port  
Dummy

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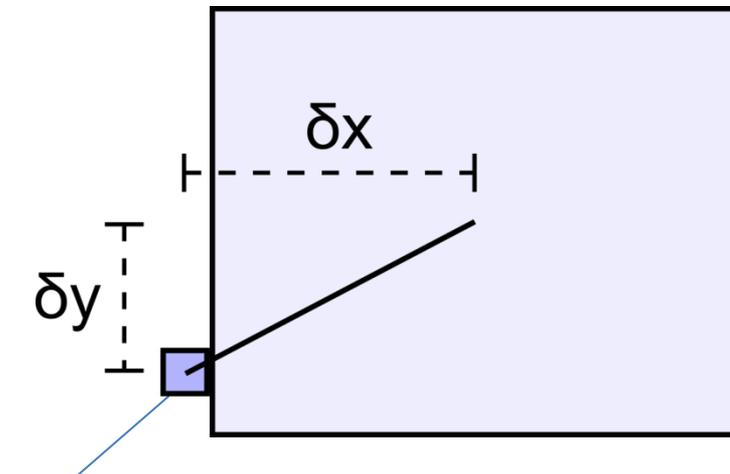
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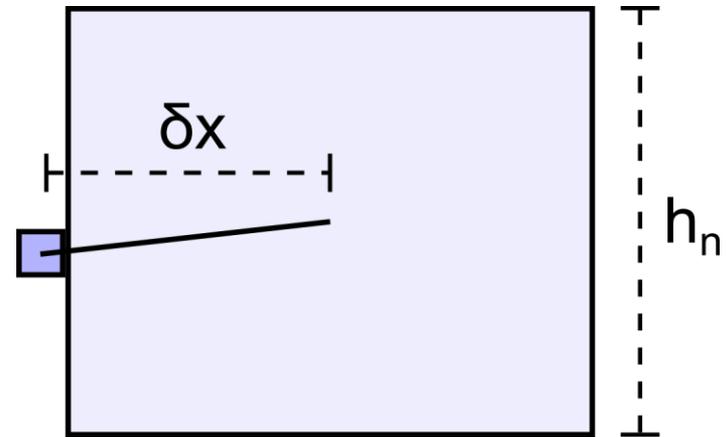


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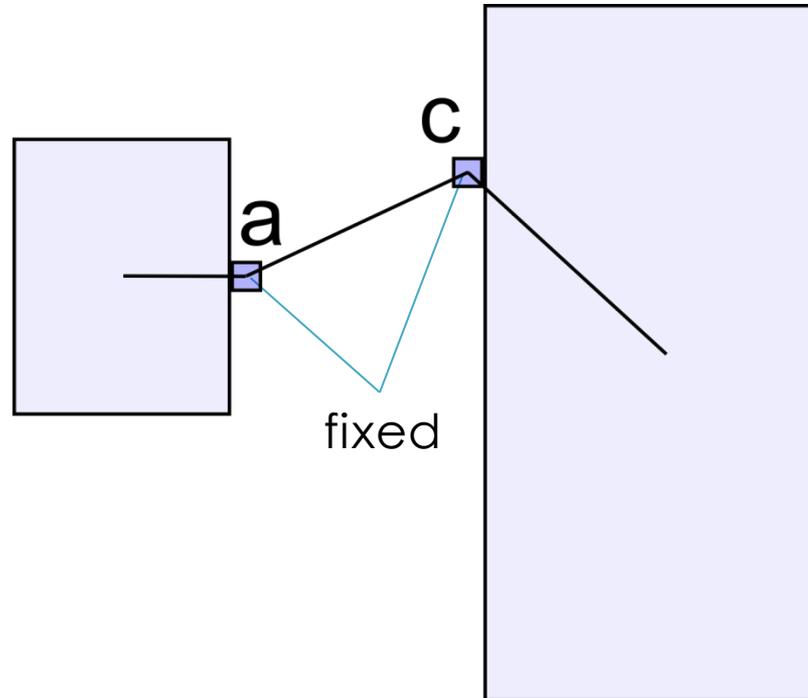


$$x_p + \delta x = x_n$$

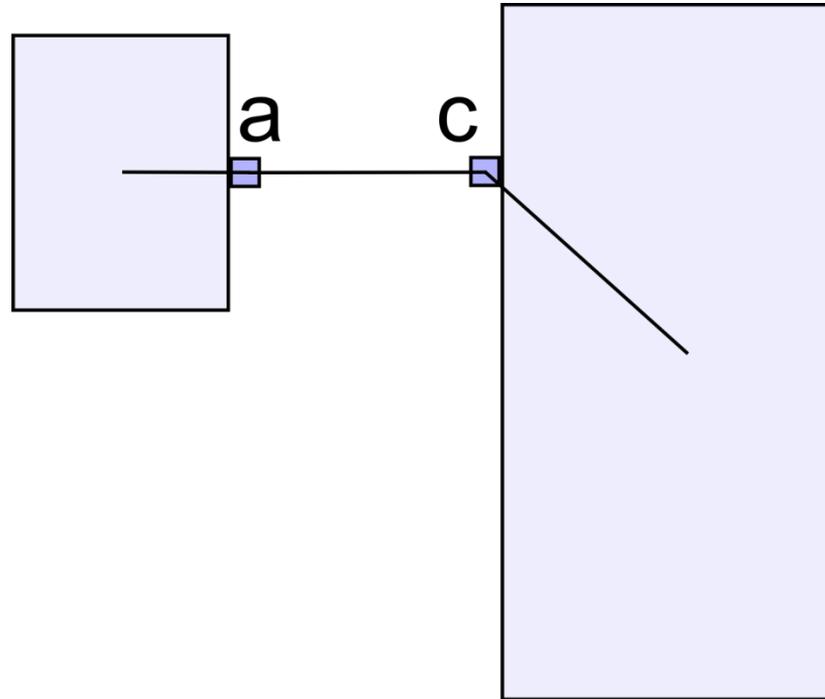
$$y_p + \frac{h_n}{2} \geq y_n$$

$$y_p - \frac{h_n}{2} \leq y_n$$

# R3 - Orthogonalizing Constraints

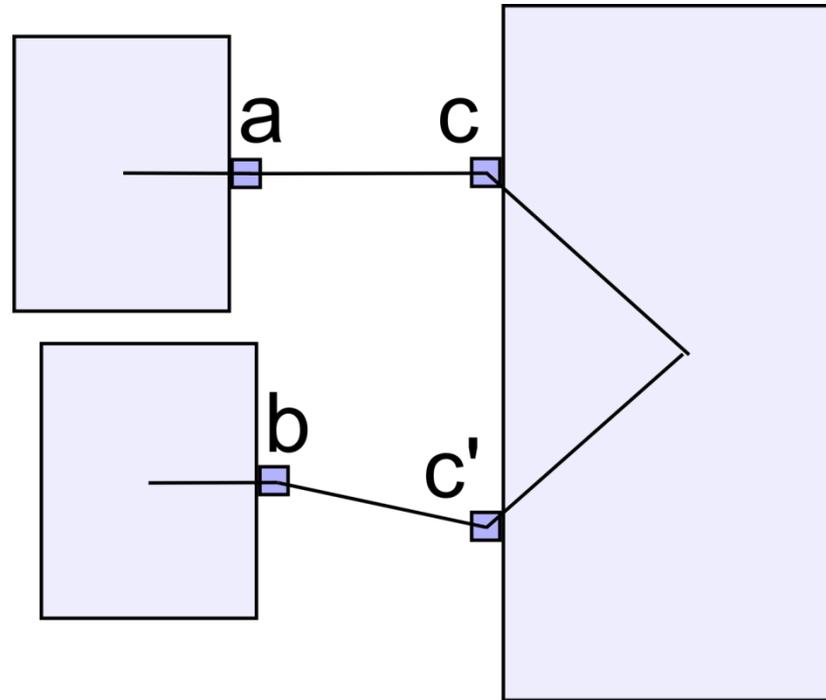


# R3 - Orthogonalizing Constraints



$$y_a = y_c$$

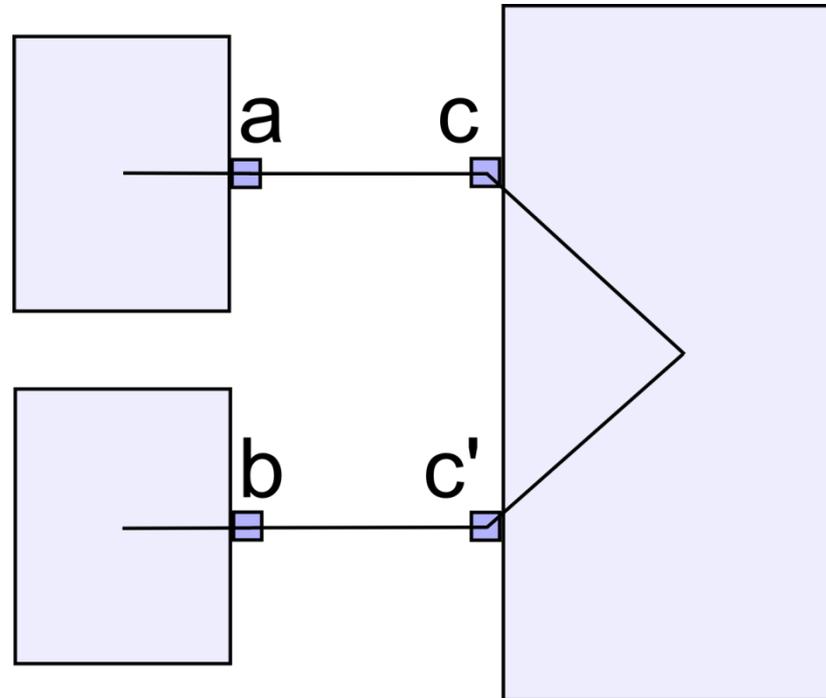
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$$y_a = y_c$$

[Kieffer et al. GD'13]

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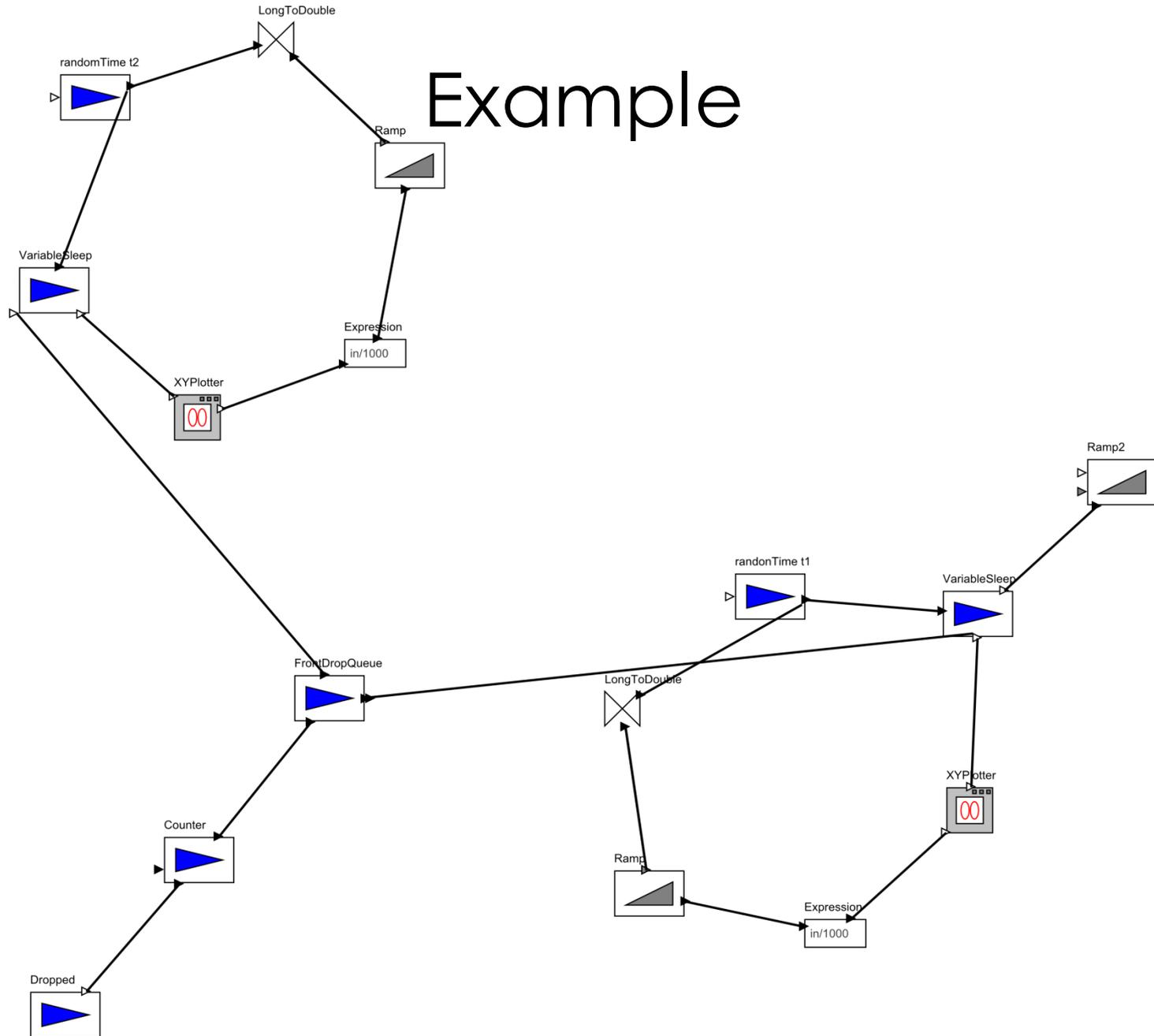


$$y_a = y_c$$

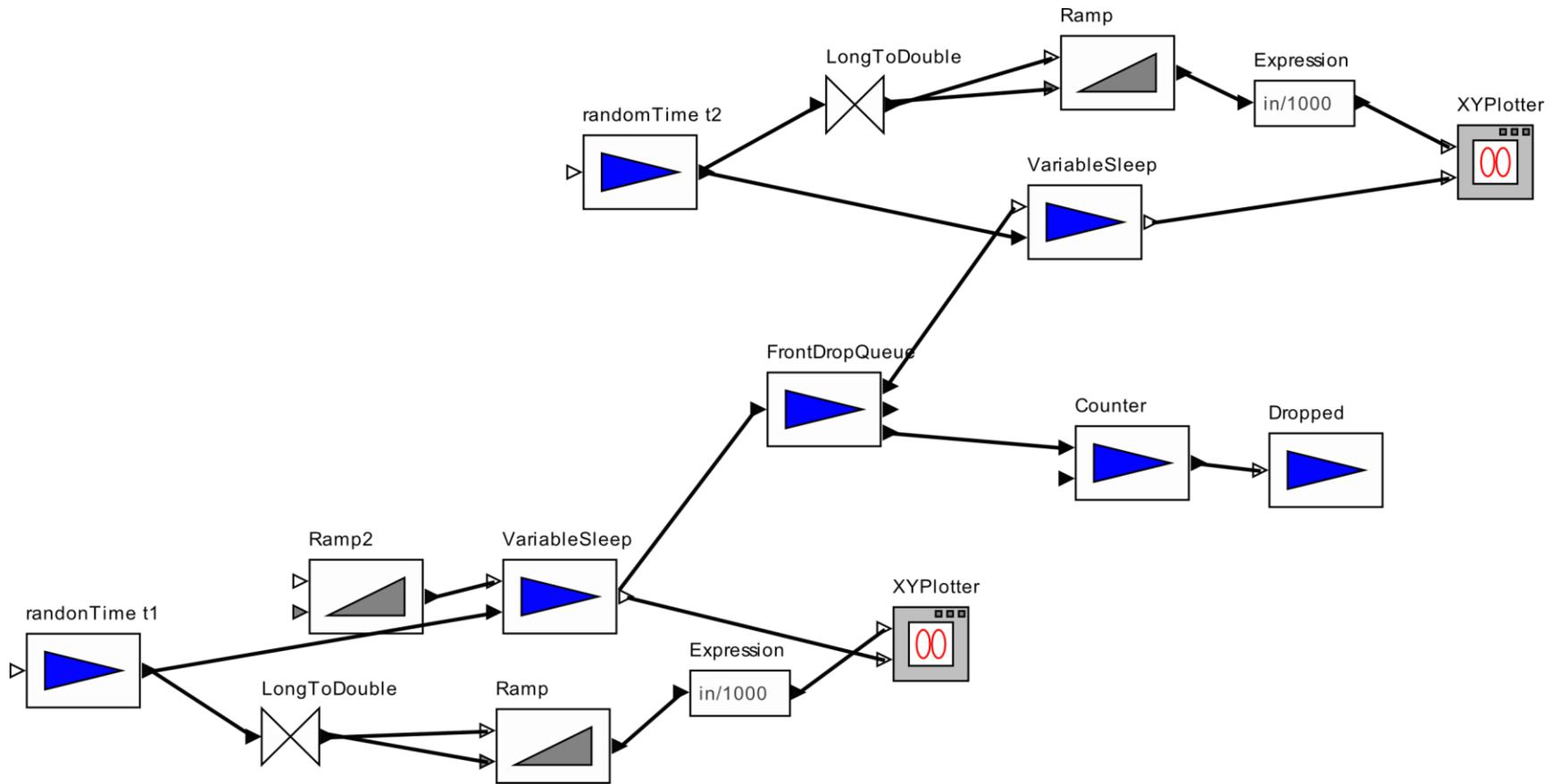
$$y_b = y_{c'}$$

[Kieffer et al. GD'13]

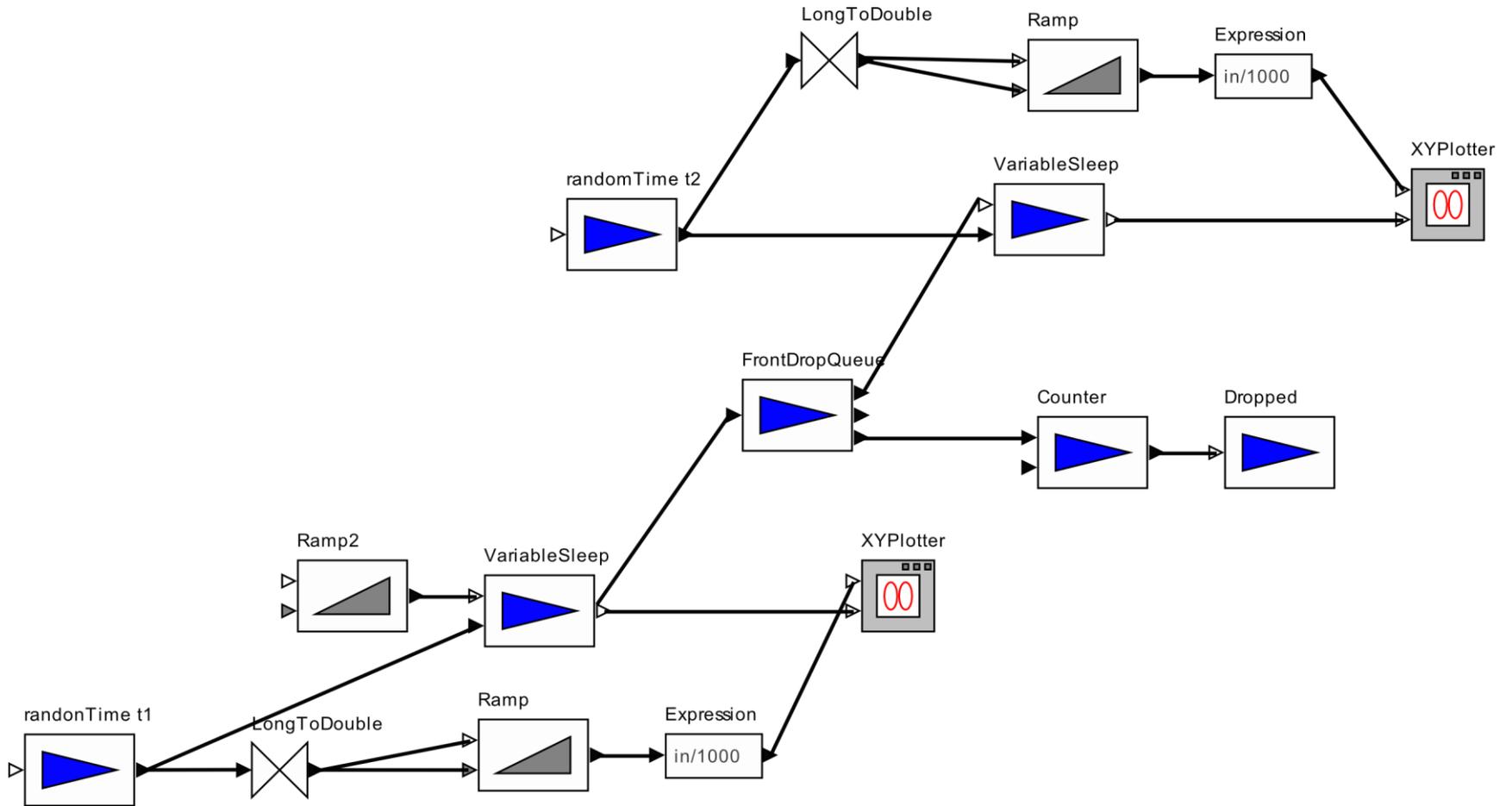
# Example



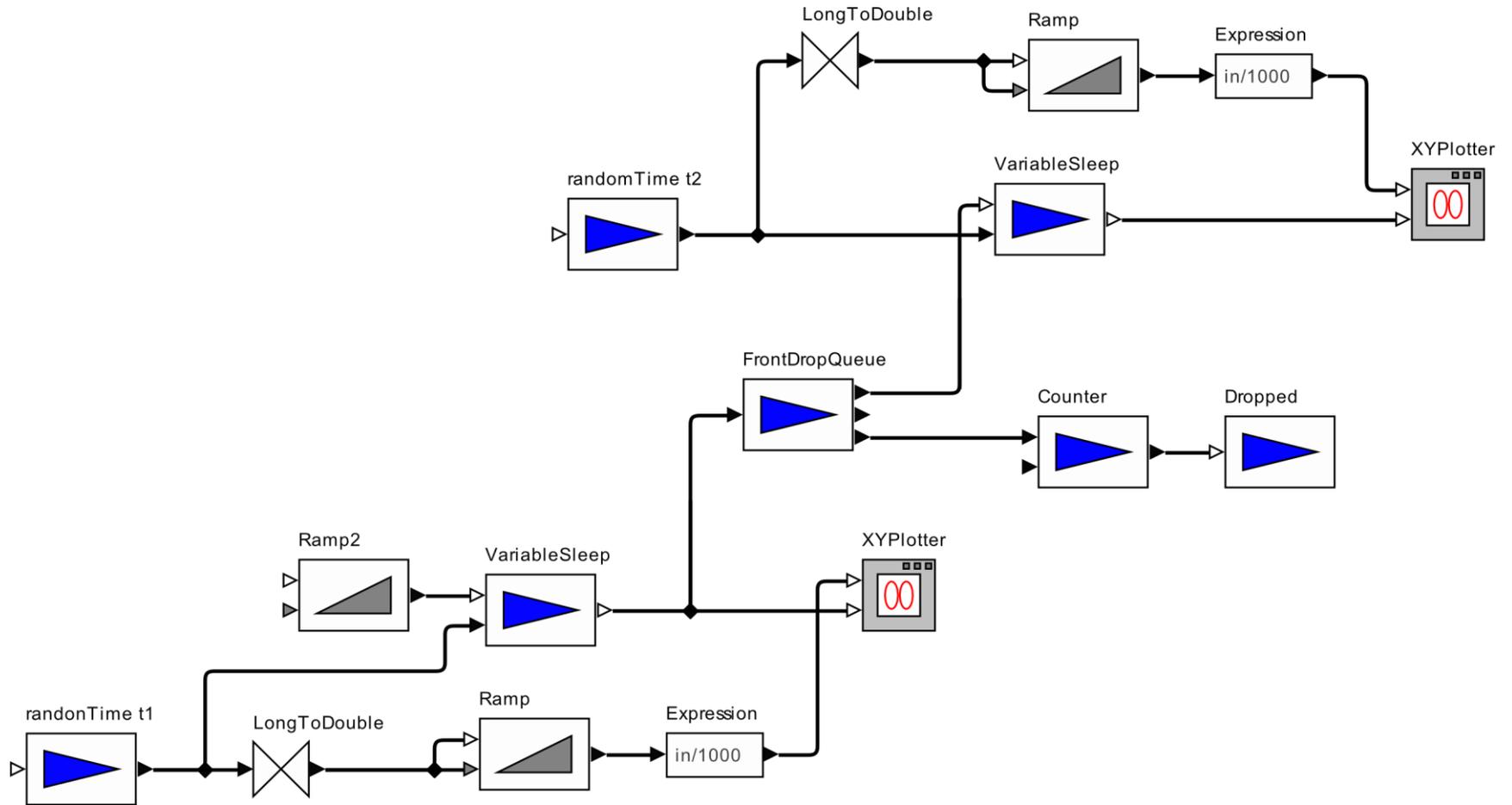
# Example



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[Wybrow et al. GD'10]

# Results

Compared to current approach (KLayer Layered)

# Results

Compared to current approach (KLayer Layered)

BETTER

Stress

# Results

Compared to current approach (KLayer Layered)

BETTER

Stress

Average edge length

# Results

Compared to current approach (KLayer Layered)

BETTER

Stress

Average edge length

Edge length variance

# Results

Compared to current approach (KLayer Layered)

BETTER

Stress

Average edge length

Edge length variance

Area and aspect ratio

# Results

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Edge length variance

Area and aspect ratio

Symmetry

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BETTER

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Average edge length

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Area and aspect ratio

Symmetry

Implementation complexity

# Results

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BETTER

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Edge length variance

Area and aspect ratio

Symmetry

Implementation complexity

WORSE

Edge crossings

# Results

Compared to current approach (KLayer Layered)

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Edge bends

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BETTER

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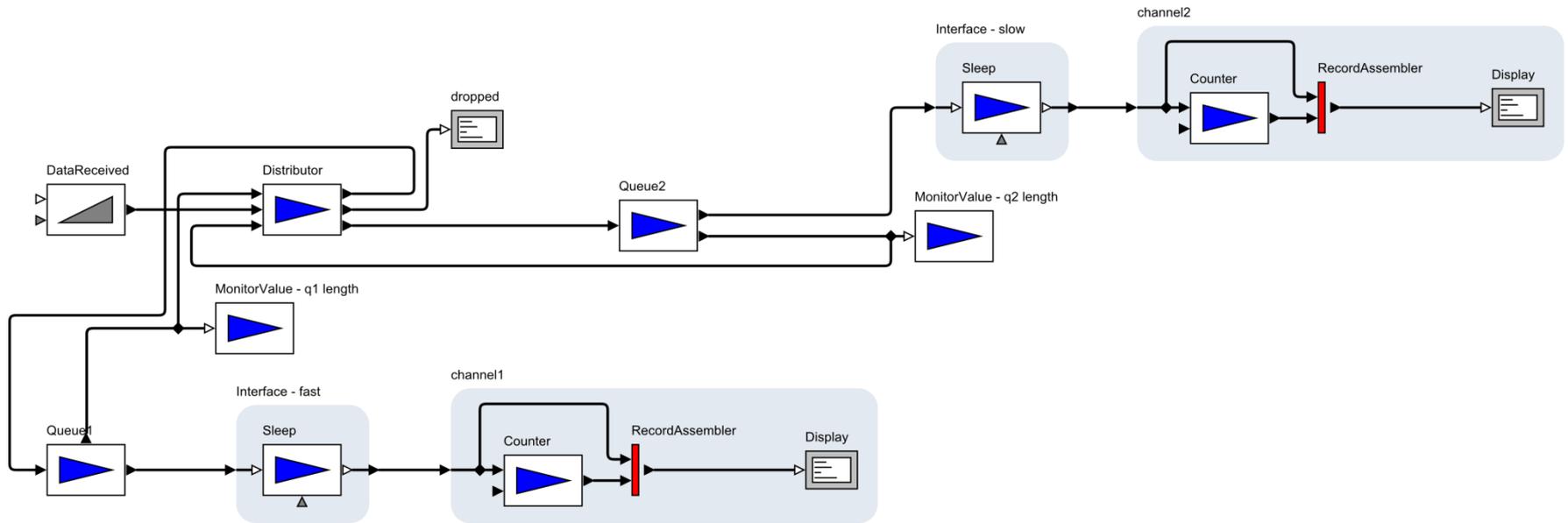
WORSE

Edge crossings

Edge bends

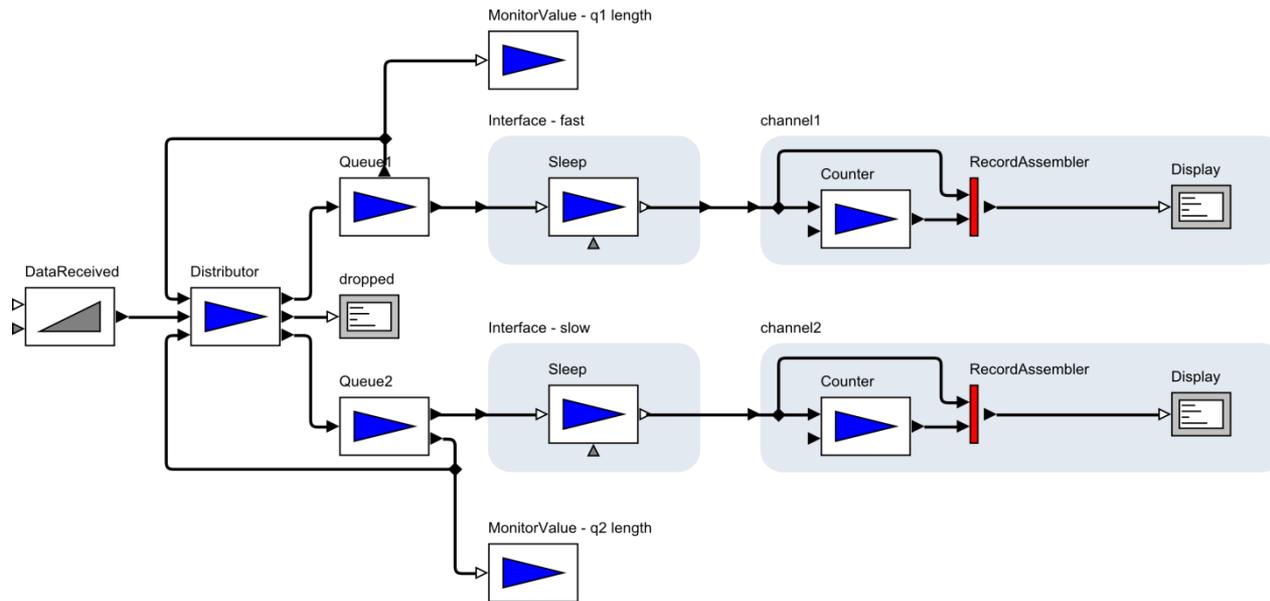
Execution time

# R4/R5 - Compound Graphs



KLay Layered

# R4/R5 - Compound Graphs



CoDaFlow

# Summary - CoDaFlow

- One goal function: minimize stress

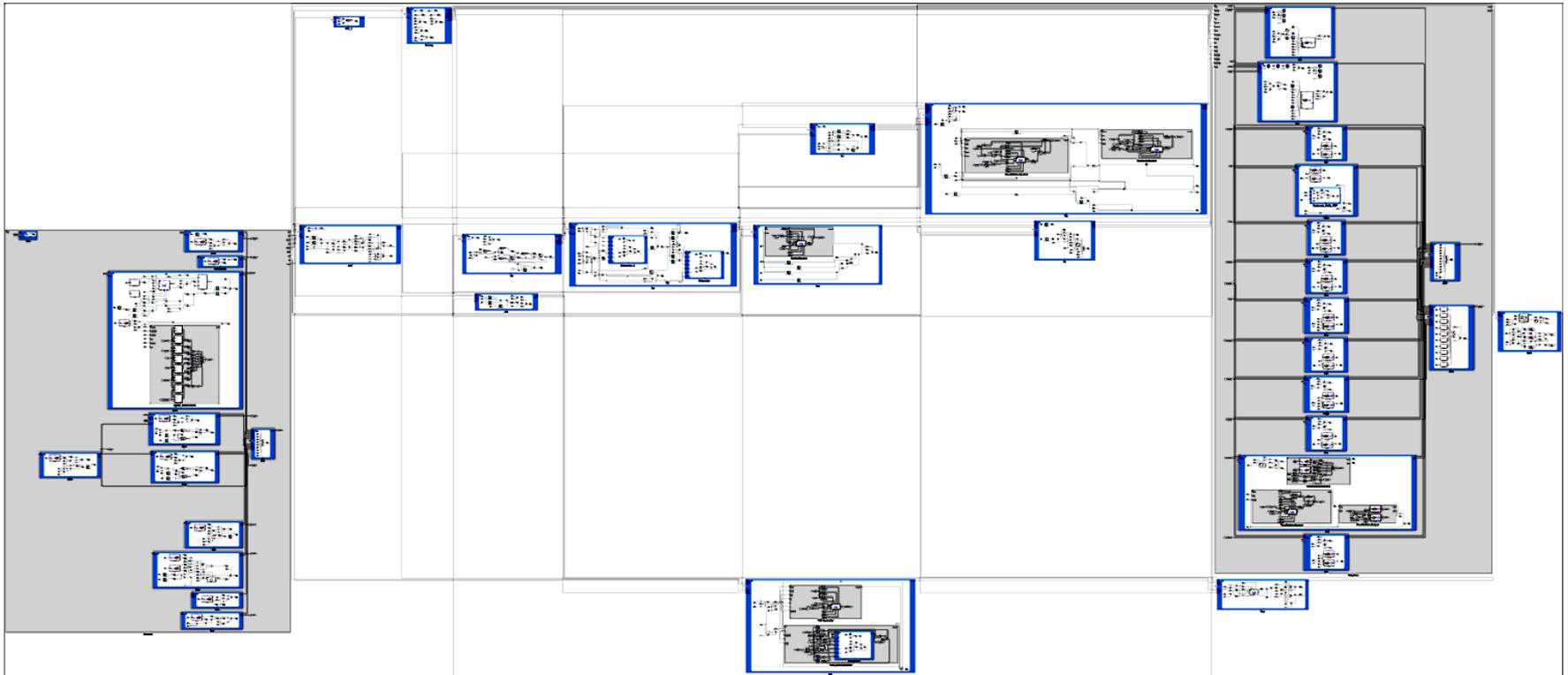
# Summary - CoDaFlow

- One goal function: minimize stress
- Incrementally add constraints
  1. No constraints
  2. + Flow constraints
  3. + Port constraints
  4. + Non-overlap constraints
  5. + Orthogonalizing constraints

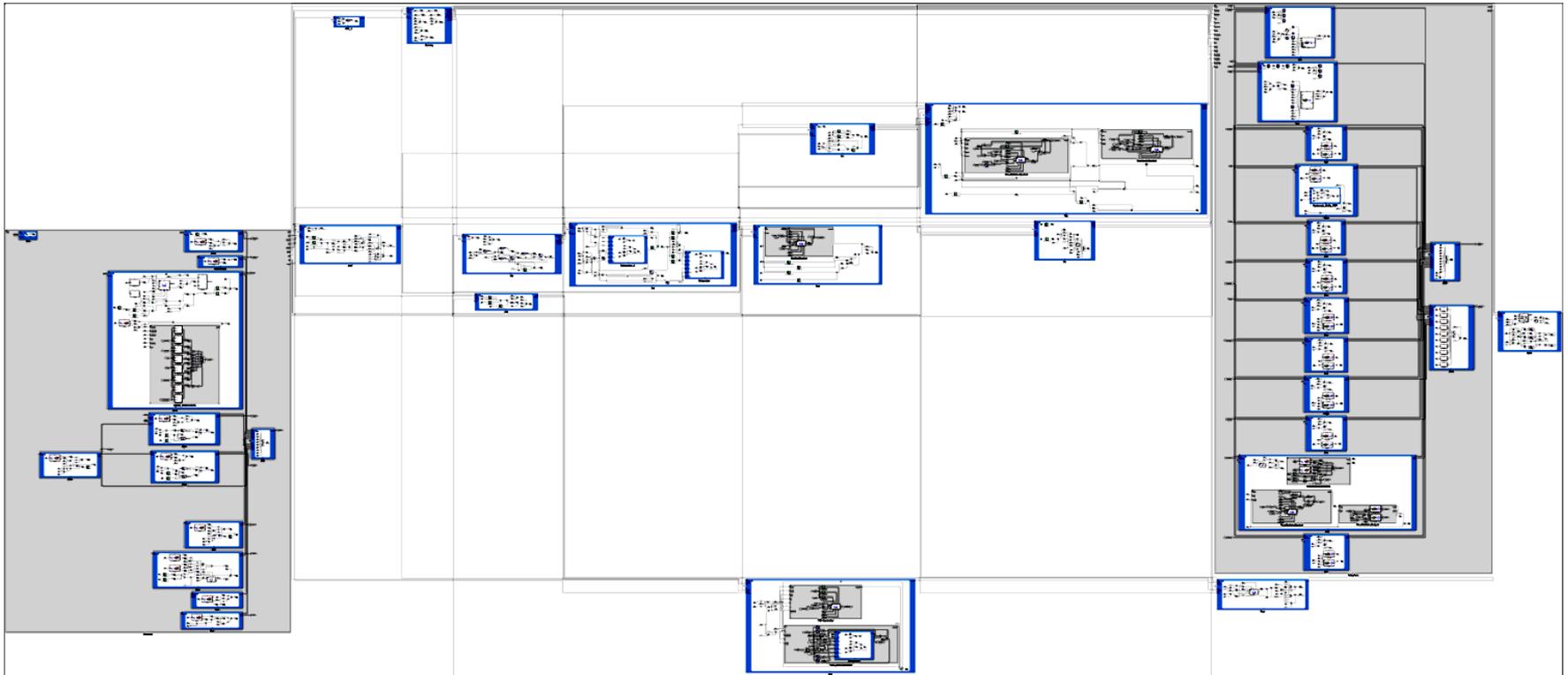
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  5. + Orthogonalizing constraints
- Orthogonal edge routing with given node positions

# Closer to Industrial Scale



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Thank you! Questions?

# References

- Schulze, C. D., Spönemann, M., & von Hanxleden, R. (2014). Drawing layered graphs with port constraints. *Journal of Visual Languages & Computing*.
- Dwyer, T., Koren, Y., & Marriott, K. (2006). IPSep-CoLa: An incremental procedure for separation constraint layout of graphs. *IEEE Transactions on Visualization and Computer Graphics*
- Dwyer, T., Marriott, K., & Wybrow, M. (2009). Topology preserving constrained graph layout. *Graph Drawing*.
- Kieffer, S., Dwyer, T., Marriott, K., & Wybrow, M. (2013). Incremental grid-like layout using soft and hard constraints. *Graph Drawing*.
- Wybrow, M., Marriott, K., & Stuckey, P. (2010). Orthogonal connector routing. *Graph Drawing*.